

FUR0007-US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

JOJI KASAI ET AL.

Serial No. 09/361,734

Filed: July 28, 1999



Art Unit: 2747

Examiner: Unknown

For: AUDIO SIGNAL PROCESSING CIRCUIT

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**INFORMATION DISCLOSURE STATEMENT**

Sir:

In accordance with the requirements of 37 CFR §§ 1.56, 1.96-1.97 and MPEP § 609, applicants, through their attorneys, hereby bring to the attention of the Examiner the references listed on the attached Form PTO-1449. A copy of each reference is enclosed for the convenience of the Examiner.

In compliance with the requirement for a concise explanation of the relevance of these foreign language documents, applicants note that Japanese Laid-Open Patent Application No. HEI 8-51698 corresponds to U.S. Patent No. 5,579,396 and 5,761,315. Copies of the U.S. references are also attached. Japanese Laid-Open Patent Application Nos. HEI-8-205297 and HEI 8-336199 disclose the filter processing using phase reversing, phase shifting and delaying circuits in order to reduce correlation between monophonic signals into two channels. These

K. Ward  
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#5 Run Rat Watch

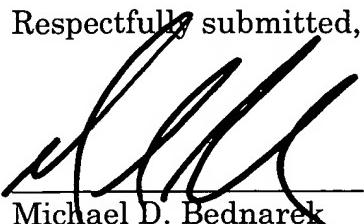
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Attorney's Docket No. FUR0007-US

documents do not, however, disclose the filter processing available for both monophonic and stereophonic signals disclosed in the present invention. These references are discussed in the specification at pages 1 and 4 and are believed to be relevant for at least that reason.

Should a first action on the merits have been issued on the same day or before this Information Disclosure Statement is filed, please accept this Information Disclosure Statement under Rule 97(c) and charge the requisite Rule 17(p) fee to our Deposit Account No. 03-3836, under Order No. FUR0007-US and proceed to consider this Information Disclosure Statement.

The above information is presented so that the Patent and Trademark Office may, in the first instance, determine any materiality thereof to the claimed invention. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

Respectfully submitted,



Michael D. Bednarek  
Registration No. 32,329

Dated: **October 14, 1999**

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<b>INFORMATION DISCLOSURE CITATION</b> <i>(Use several sheets if necessary)</i>				Docket Number (Optional) <b>FUR0007-US</b>		Application Number <b>09/361,734</b>	
				Applicant(s) <b>JOJI KASAI ET AL.</b>		Filing Date <b>JULY 28, 1999</b>	
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		5,579,396	11/26/96	Iida et al.	381	18	08/01/94
		5,761,315	06/02/98	Iida et al.	381	18	08/06/96
FOREIGN PATENT DOCUMENTS							
REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
	8-51698	02/20/96	Japan	H04S	1/00		✓
	8-205297	08/09/96	Japan	H04S	1/00		✓
	8-336199	12/17/96	Japan	H04S	5/00		✓
OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>							
		<b>RECEIVED</b> <b>OCT 15 1999</b>					
EXAMINER				DATE CONSIDERED		<b>Group 2700</b>	

**EXAMINER**

**DATE CONSIDERED**

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**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.